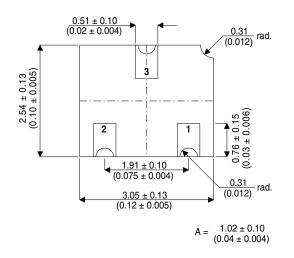
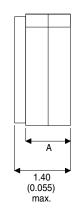


### 2N7000CSM

#### **MECHANICAL DATA**

Dimensions in mm (inches)





## **SOT23 CERAMIC** (LCC1 PACKAGE)

#### **Underside View**

PAD 1 - Gate

PAD 2 - Source

PAD 3 - Drain

# **N-CHANNEL ENHANCEMENT MODE MOS TRANSISTOR**

### **FEATURES**

- V<sub>(BR)DSS</sub> = 60V
- $RDS_{(ON)} = 5\Omega$
- I<sub>D</sub> = 200mA
- Hermetic Ceramic Surface Mount package
- Screening Options Available

## **ABSOLUTE MAXIMUM RATINGS** (T<sub>CASE</sub> = 25°C unless otherwise stated)

$\overline{V_{DS}}$	Drain – Source Voltage		60V	
$V_{GS}$	Gate - Source Voltage	Gate – Source Voltage		
$I_{D}$	Drain Current	$@ T_{CASE} = 25^{\circ}C$	200mA	
$I_{DM}$	Pulsed Drain Current *		500mA	
$P_{D}$	Power Dissipation	$@ T_{CASE} = 25^{\circ}C$	300mW	
T <sub>j</sub>	Operating Junction Temperature Range		−55 to 150°C	
T <sub>stg</sub>	Storage Temperature Range		−55 to 150°C	

<sup>\*</sup> Pulse width limited by maximum junction temperature.

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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Document Number 7022

Issue: 1



## 2N7000CSM

# **ELECTRICAL CHARACTERISTICS** (T<sub>CASE</sub> = 25°C unless otherwise stated)

	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
	STATIC CHARACTERISTICS	•	<u>.</u>				
V <sub>(BR)DSS</sub>	Drain – Source Breakdown Voltage	$V_{GS} = 0V$	$I_D = 10\mu A$	60	70		V
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$	$I_D = 0.25 \text{mA}$	8.0		3.0	1
I <sub>GSS</sub>	Gate – Body Leakage Current	$V_{GS} = \pm 20V$	$V_{DS} = 0V$			-10	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V	$V_{GS} = 0V$			1.0	μΑ
			T <sub>CASE</sub> = 125°C			1.0	mA
I <sub>D(on)*</sub>	On-State Drain Current	V <sub>DS</sub> ≥2V <sub>DS(ON)</sub>	$V_{GS} = 4.5V$	75			mA
R <sub>DS(on)*</sub>	Drain – Source On Resistance	V <sub>GS</sub> = 10V				5	Ω
		I <sub>D</sub> = 0.5A	T <sub>CASE</sub> = 125°C			9	
V <sub>DS(on)*</sub>	Drain – Source On Voltage	$V_{GS} = 4.5V$	I <sub>D</sub> = 75mA			0.4	V
		V <sub>GS</sub> = 10V	$I_D = 0.5A$			2.5	
g <sub>FS*</sub>	Forward Transconductance	V <sub>GS</sub> = 10V	$I_D = 0.5A$	100			ms
	DYNAMIC CHARACTERISTICS	•					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25V				60	pF
C <sub>oss</sub>	Output Capacitance	$V_{GS} = 0V$	-			25	
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz	-			5	
	SWITCHING CHARACTERISTICS	•					
t <sub>ON</sub>	Turn-On Time	$V_{DD} = 30V$ $R_{L} = 150\Omega$ $I_{D} = 0.2A$	$V_{GEN} = 10V$ $R_G = 25\Omega$			10	- ns
t <sub>OFF</sub>	Turn-Off Time					10	

<sup>\*</sup> Pulse Test: PW = 80  $\mu s$  ,  $\delta \leq$  1%

	Parameter	Min.	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient			416	°C/W

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